

FIG. 1

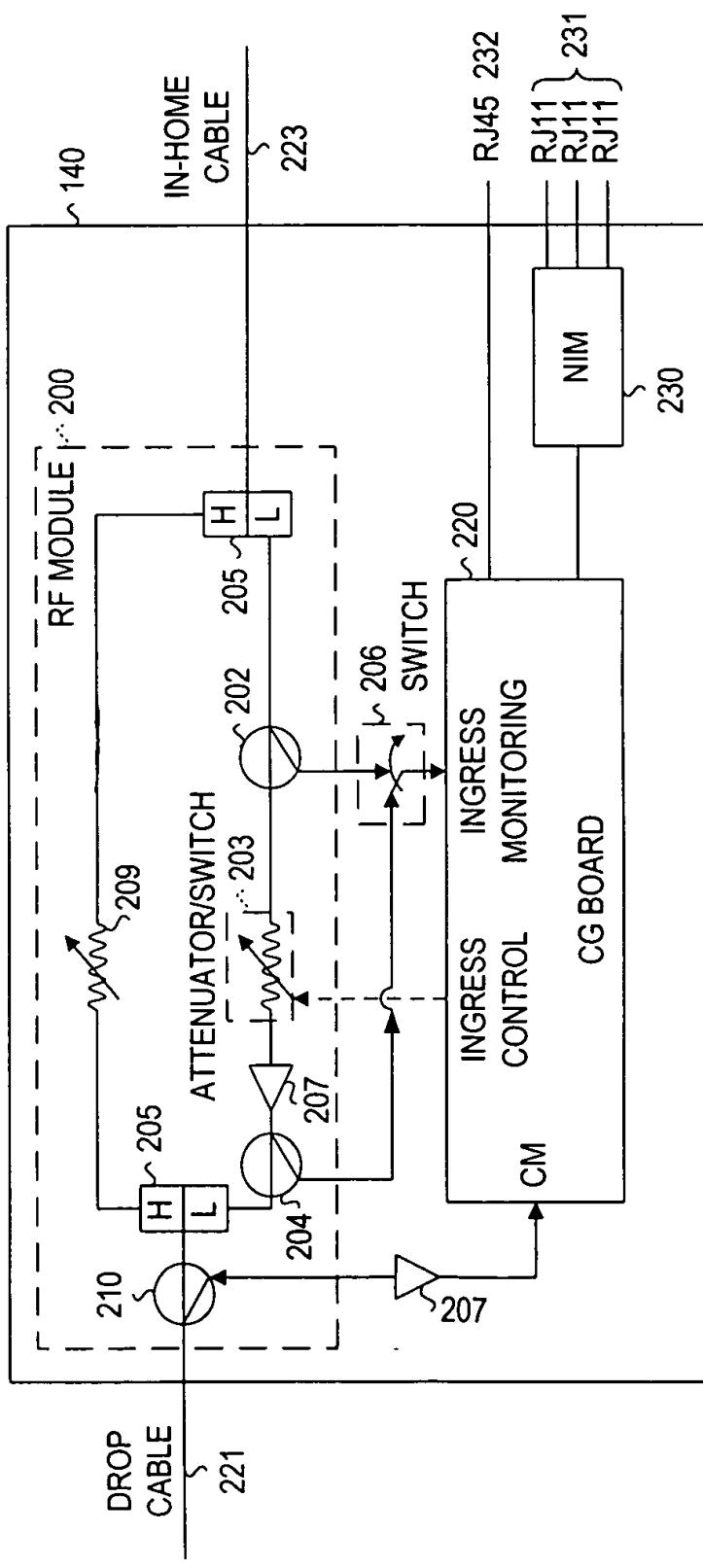


FIG. 2A

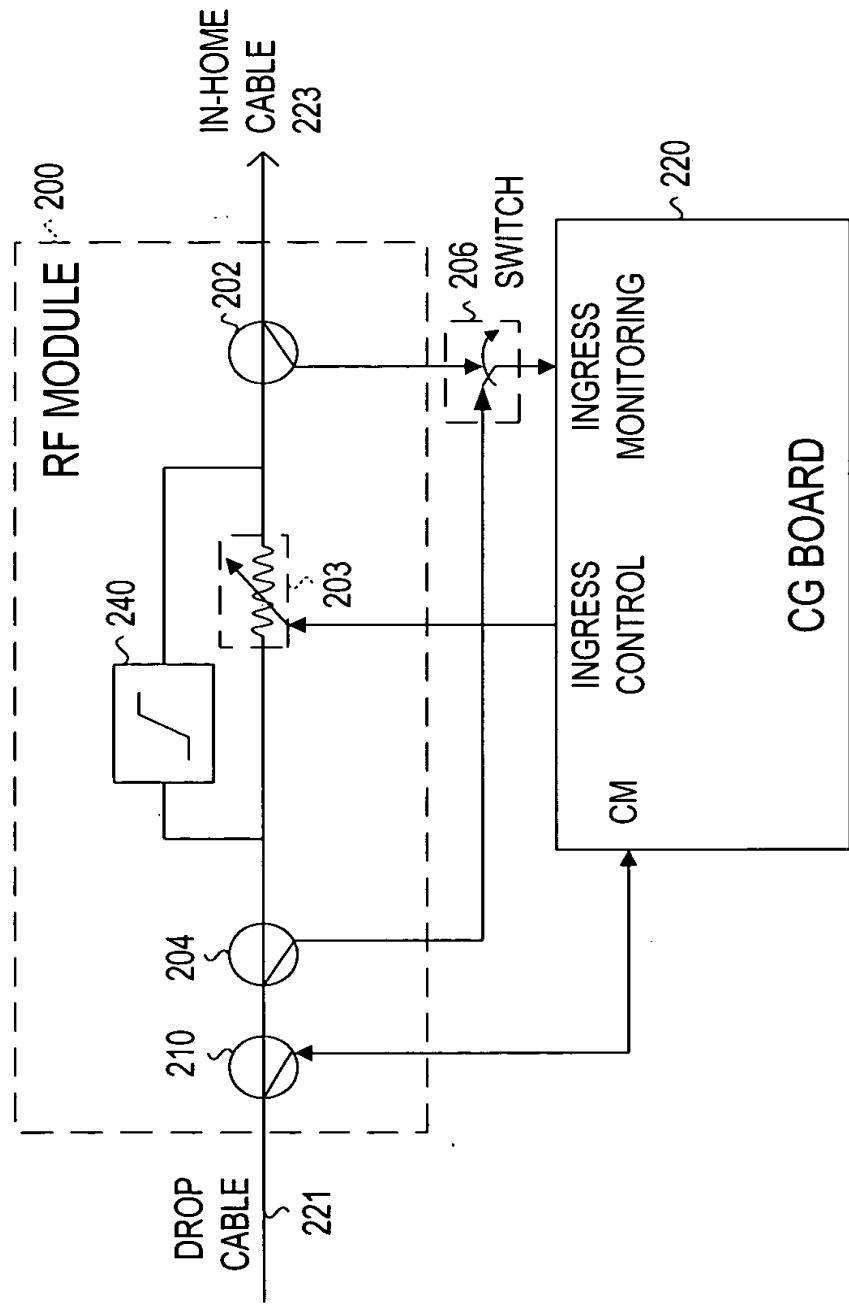


FIG. 2B

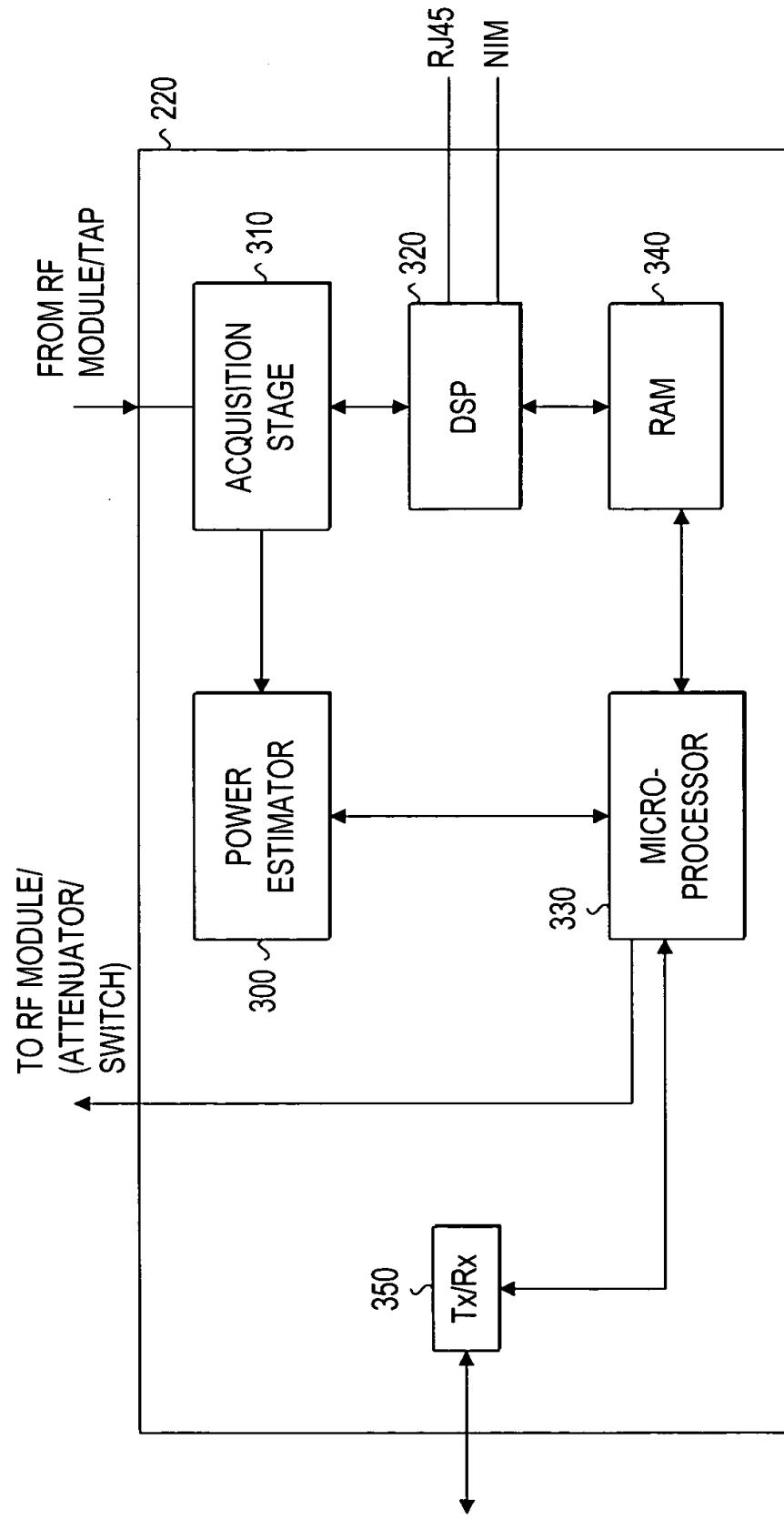


FIG. 3A

5/11

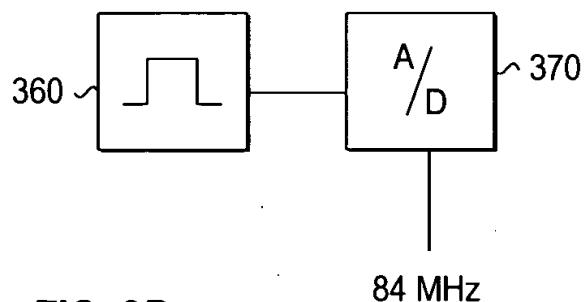


FIG. 3B

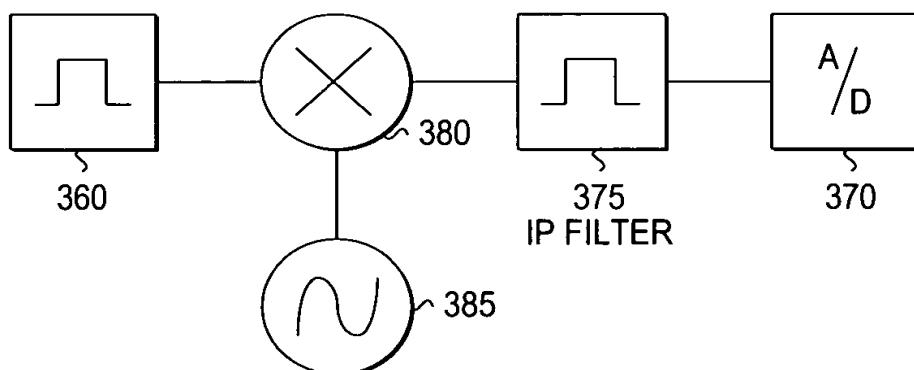


FIG. 3C

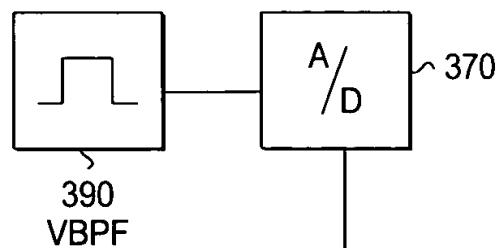


FIG. 3D

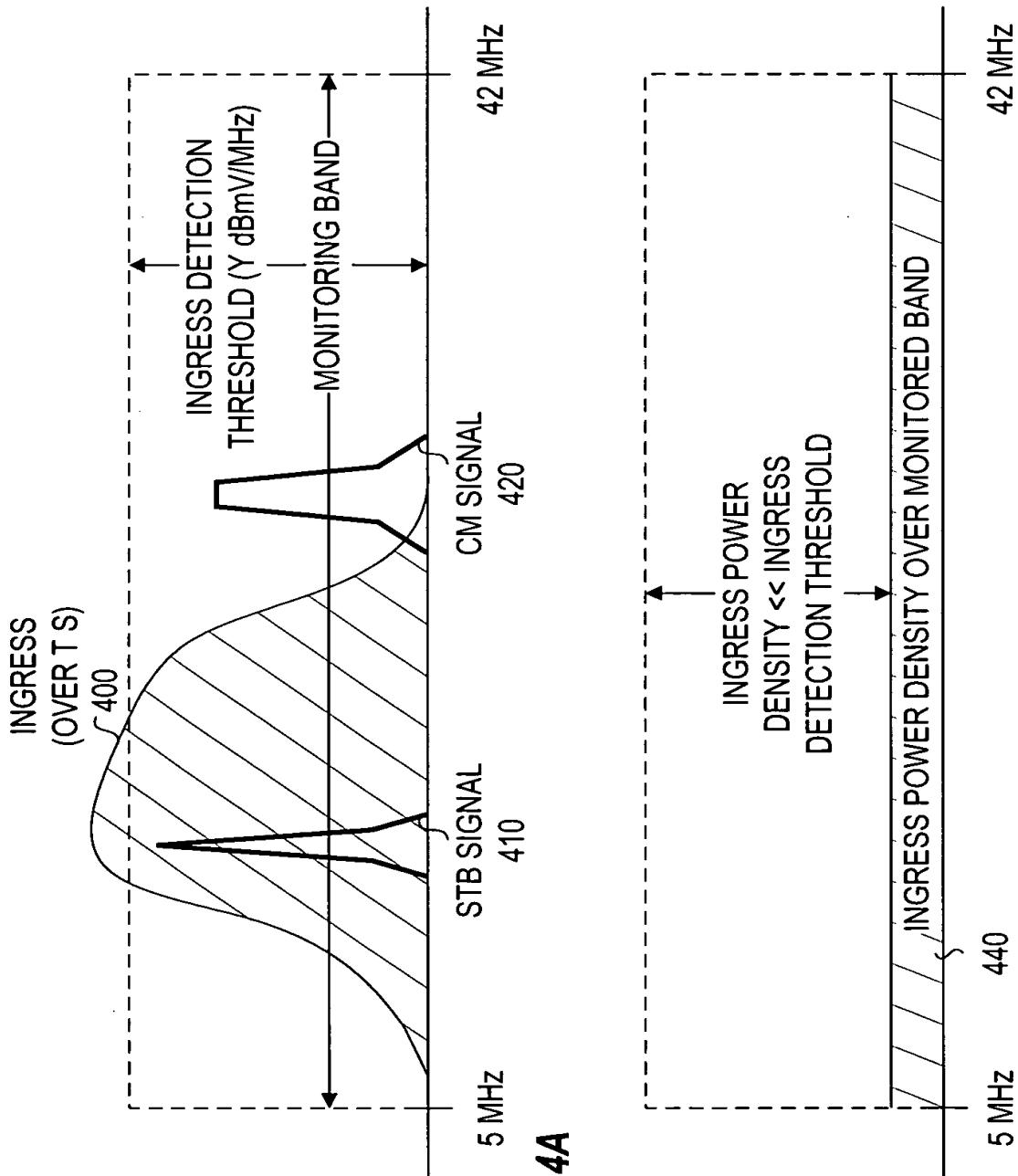


FIG. 4A

FIG. 4B

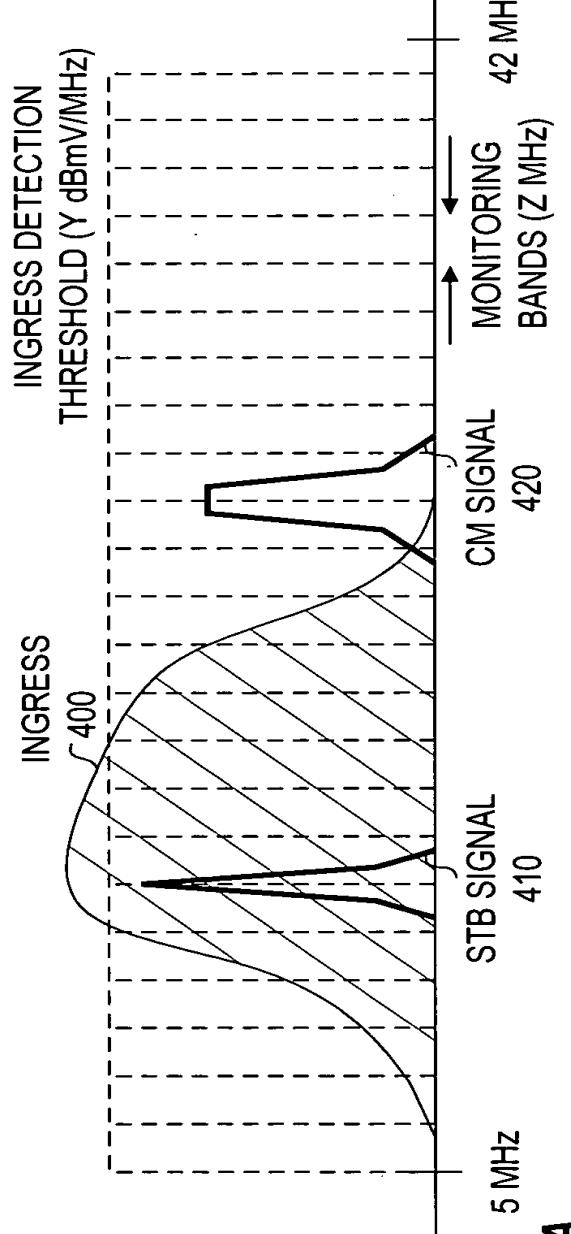


FIG. 5A

INGRESS POWER DENSITY >
INGRESS DETECTION THRESHOLD

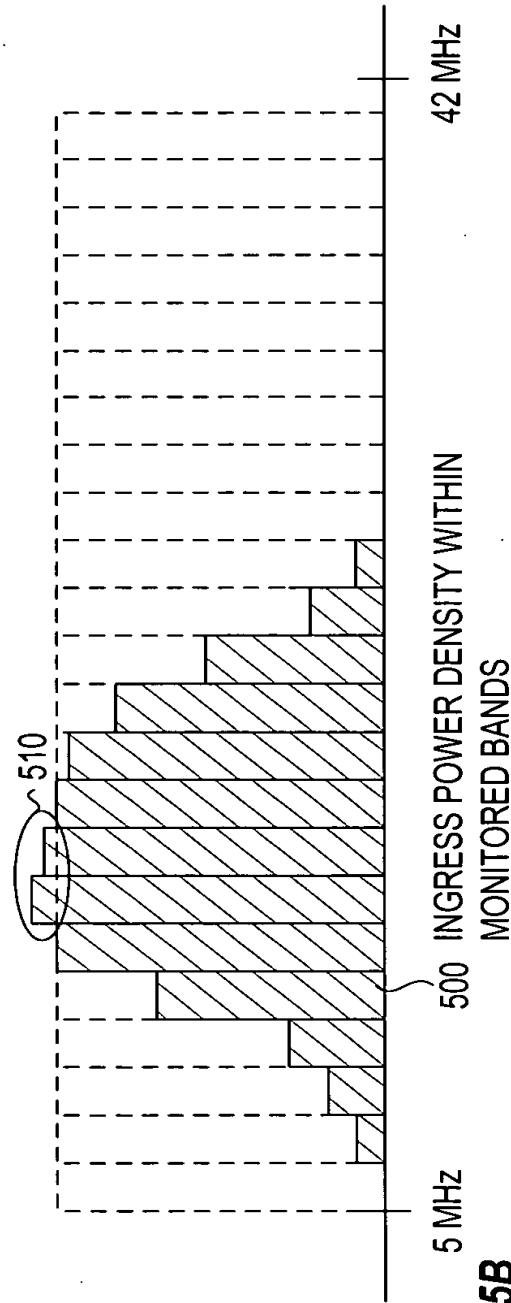


FIG. 5B

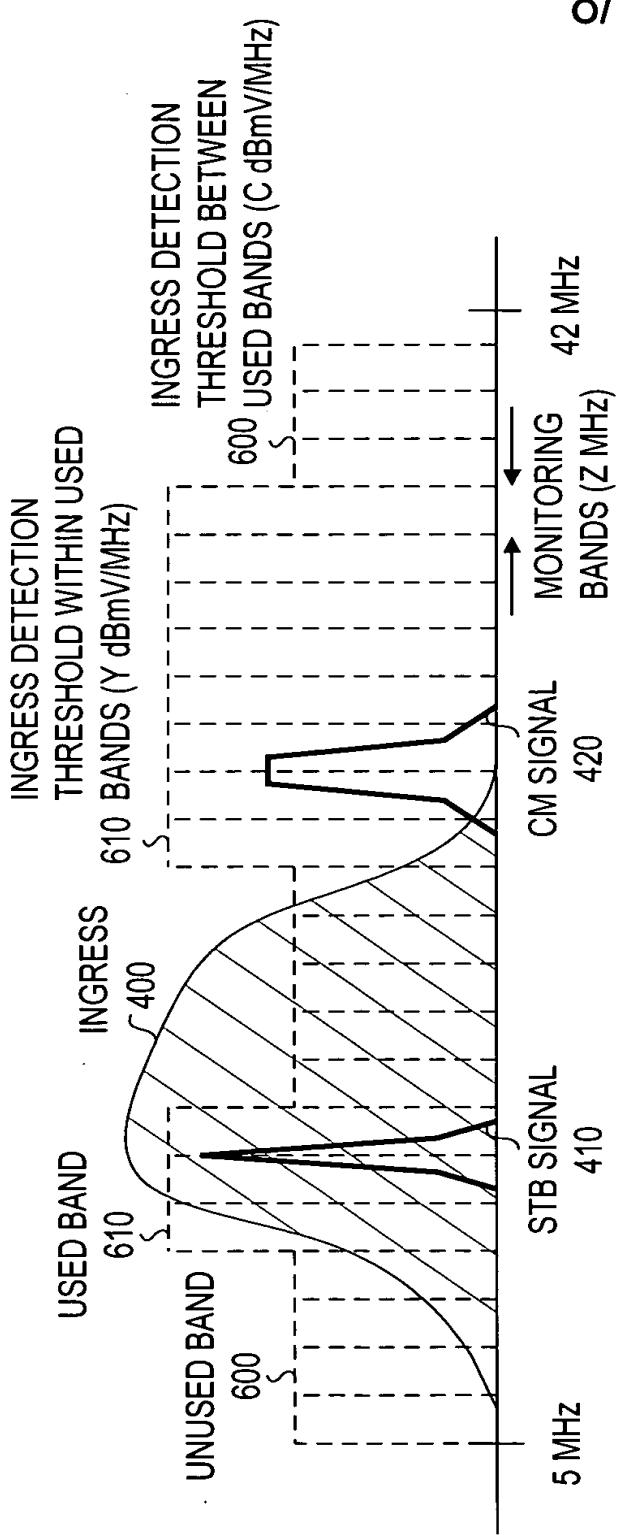


FIG. 6A

INGRESS POWER DENSITY >
INGRESS DETECTION THRESHOLDS

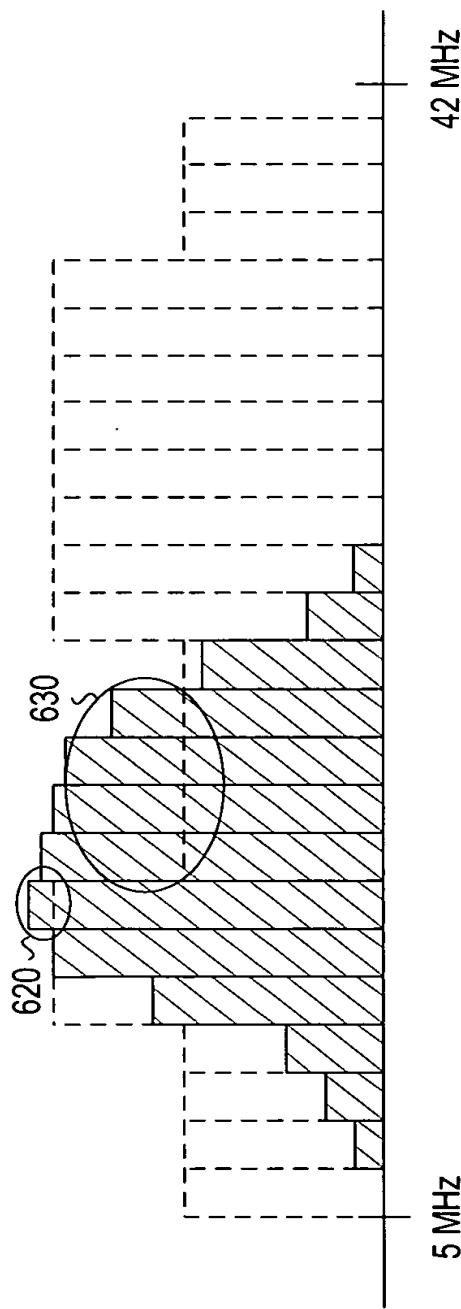


FIG. 6B

9/11

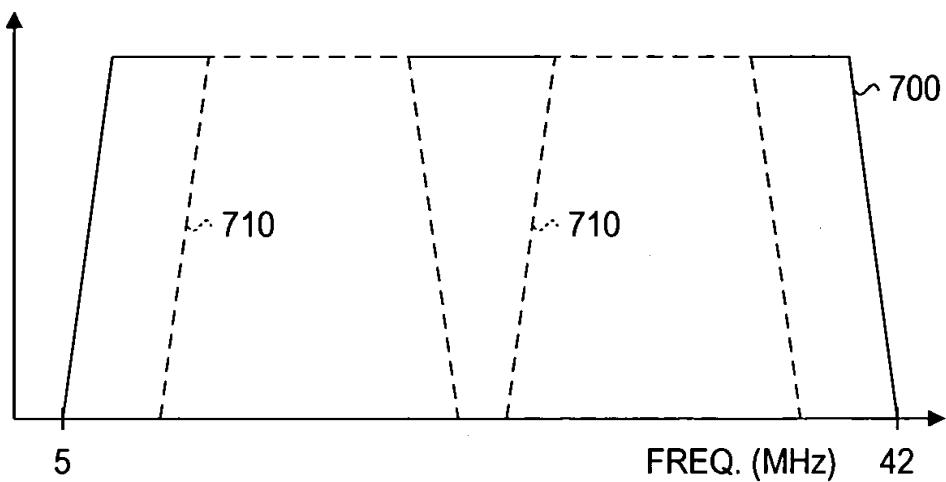


FIG. 7A

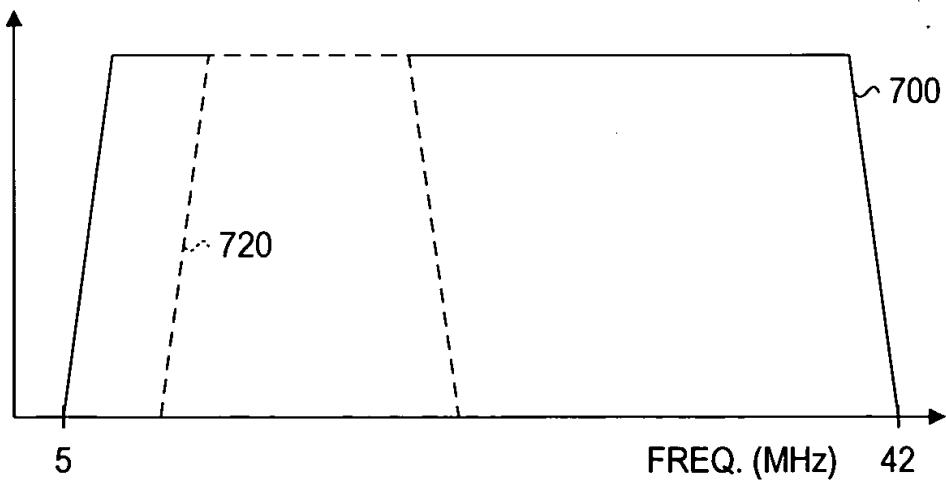


FIG. 7B

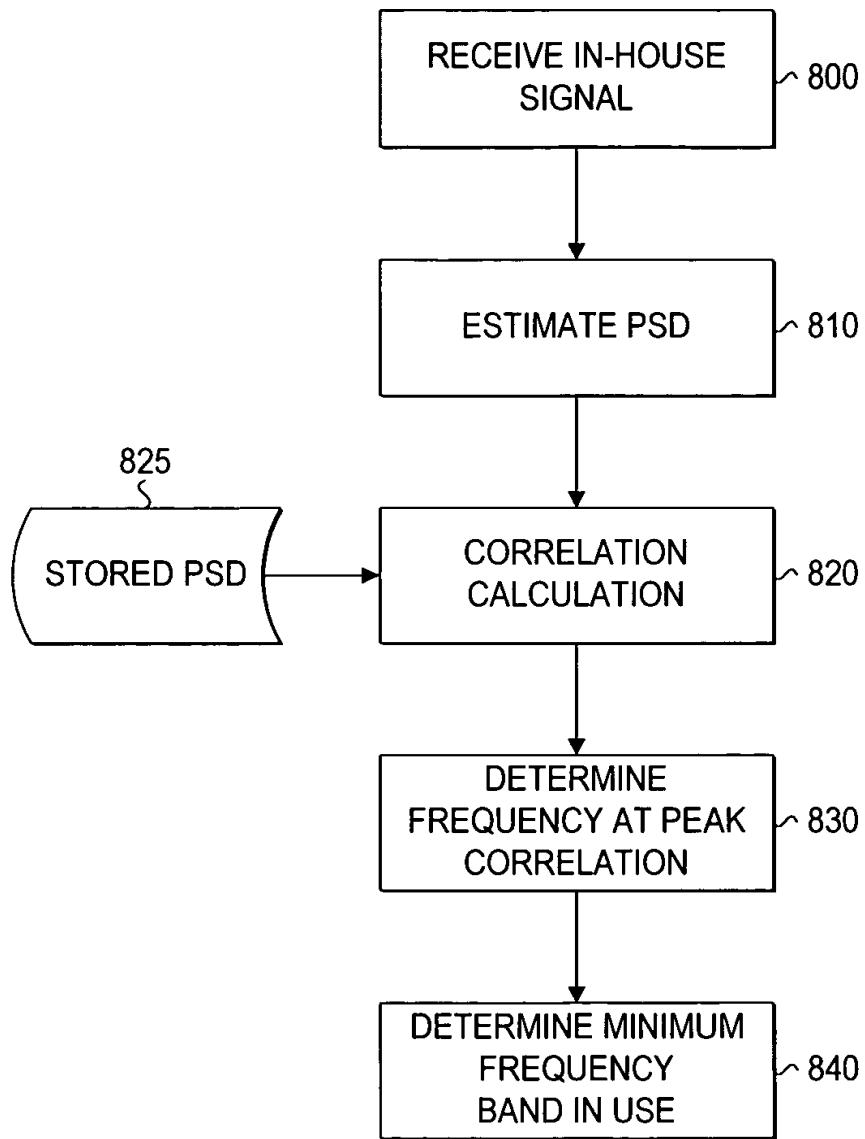
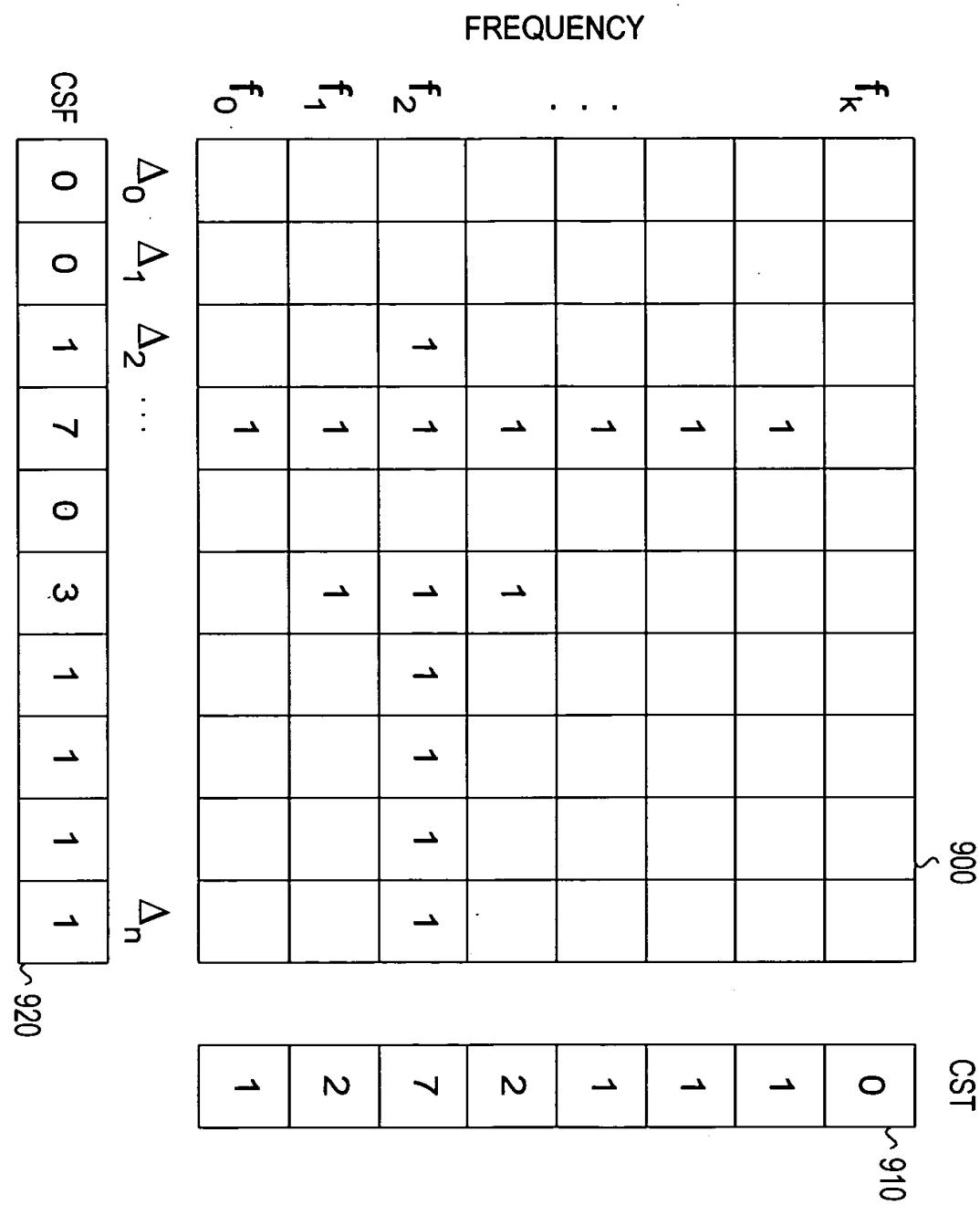
**FIG. 8**

FIG. 9



11/1